

| Ref # | Hits | Search Query   | DBs                                    | Default Operator | Plurals | Time Stamp       |
|-------|------|--|--|------------------|---------|------------------|
| S1    | 200  | 703/28.ccls.   | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/01/15 14:48 |
| S3    | 158  | ((programmable near clock) and (fpga or pld or (programmable adj logic adj device)) and @ad<"20010510" | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/01/16 17:57 |
| S5    | 128  | ((programmable near clock) same processor) and @ad<"20010510"  | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/01/16 17:57 |